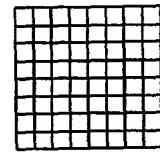


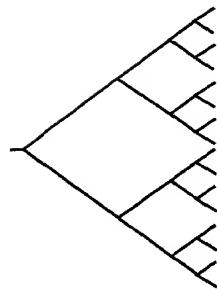
"H" - TREE

Fig. 1A
(PRIOR ART)



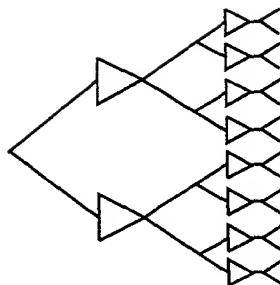
GRID

Fig. 1B
(PRIOR ART)



BALANCED TREE

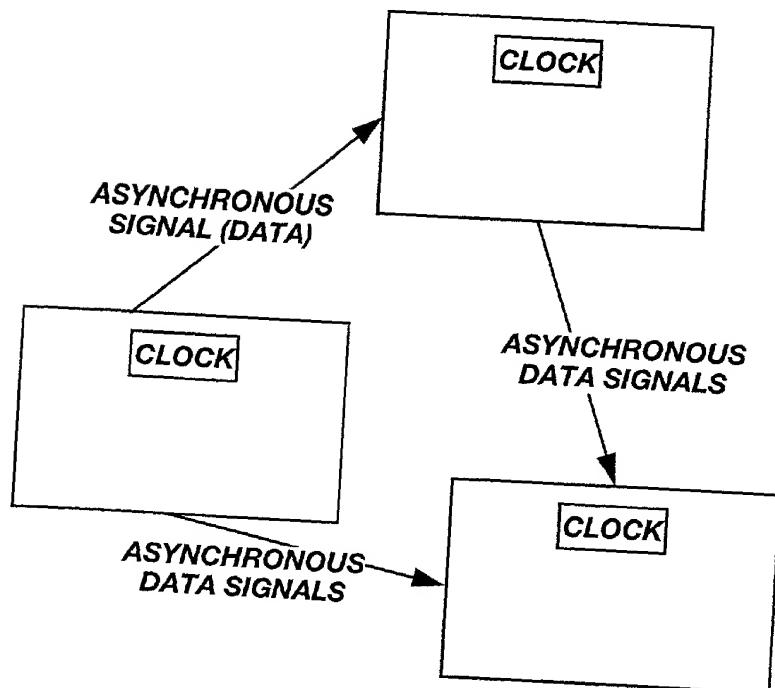
Fig. 1C
(PRIOR ART)



BALANCED TREE WITH BUFFERS

Fig. 1D
(PRIOR ART)

Inventor: Leonard Forbes
Title: Technique to Simultaneously Distribute Clock
Signals and Data on Integrated Circuits, Interposers,
and Circuit Boards
Attorney Docket No. 4720 (00-1120)
Sheet 0002 of 0006
Contact: Devin R. Jensen (801) 532-1922



*Fig. 2
(PRIOR ART)*

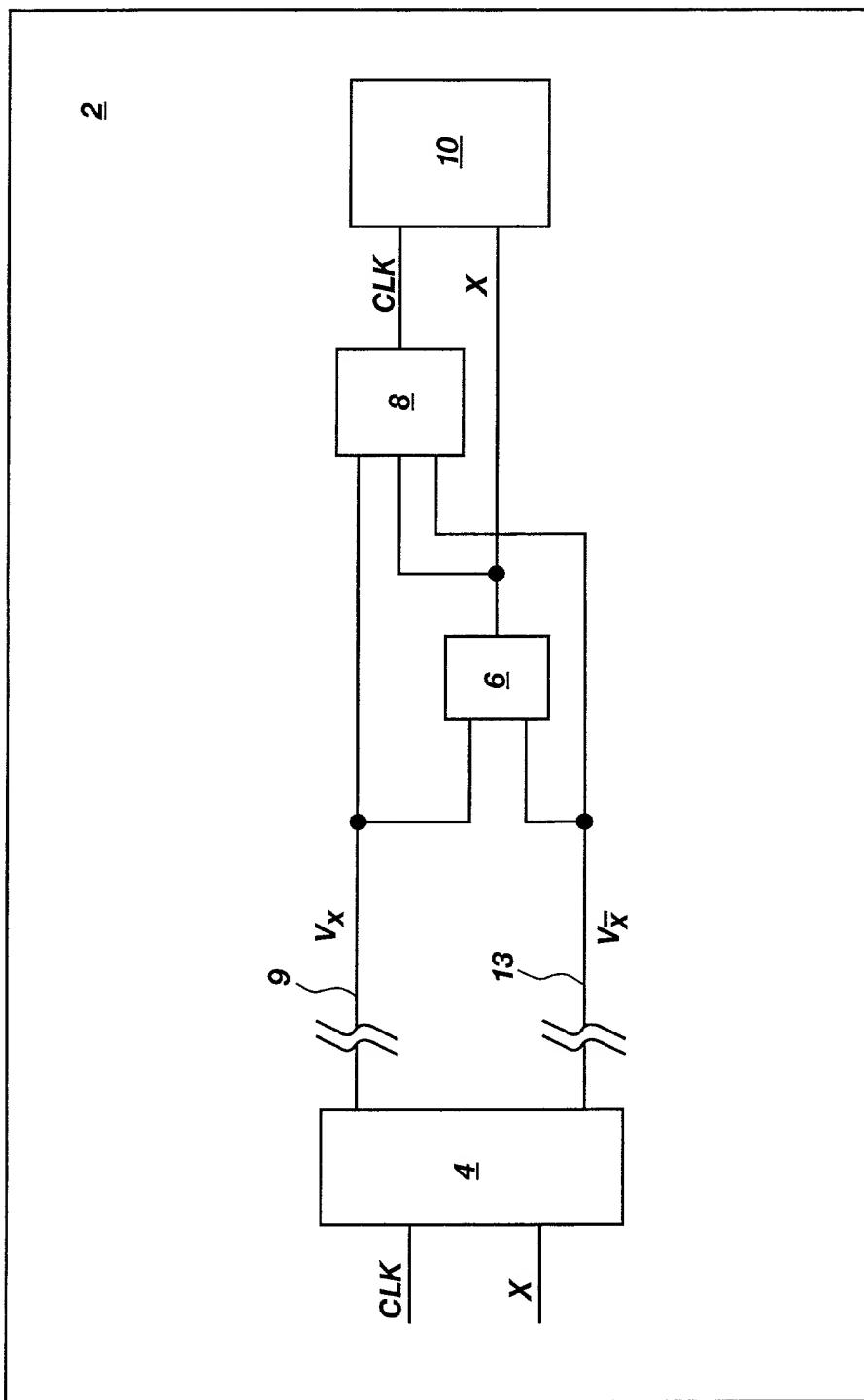


Fig. 3

Inventor: Leonard Forbes
 Title: Technique to Simultaneously Distribute Clock Signals and Data on Integrated Circuits, Interposers, and Circuit Boards
 Attorney Docket No. 4720 (00-1120)
 Sheet 0004 of 0006
 Contact: Devin R. Jensen (801) 532-1922

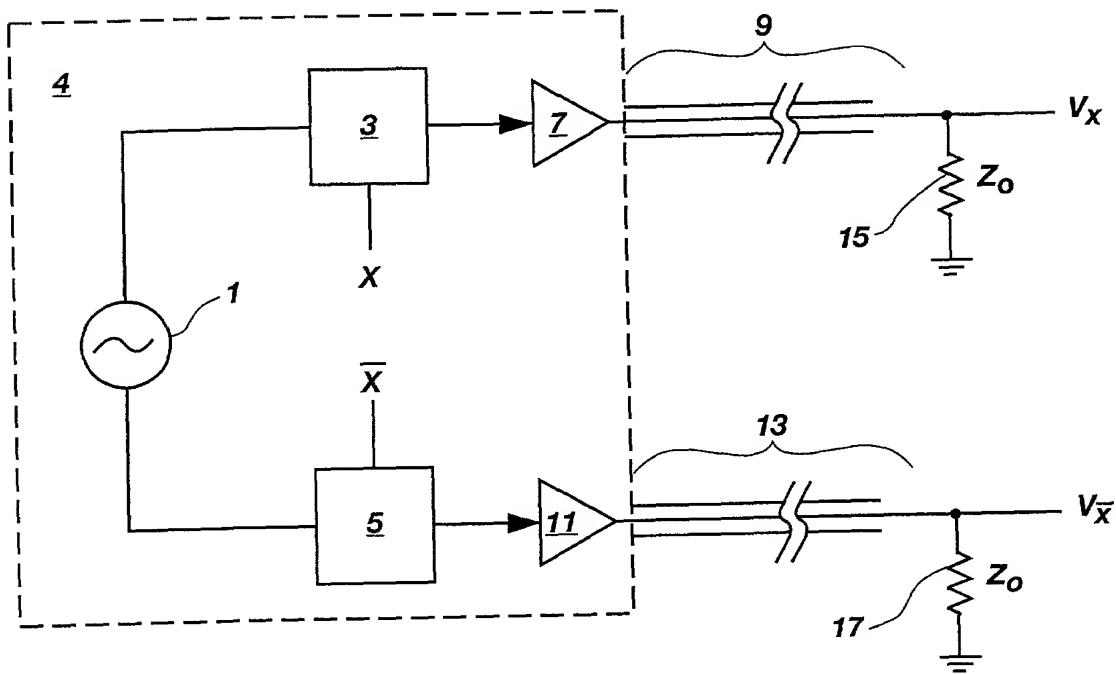


Fig. 4

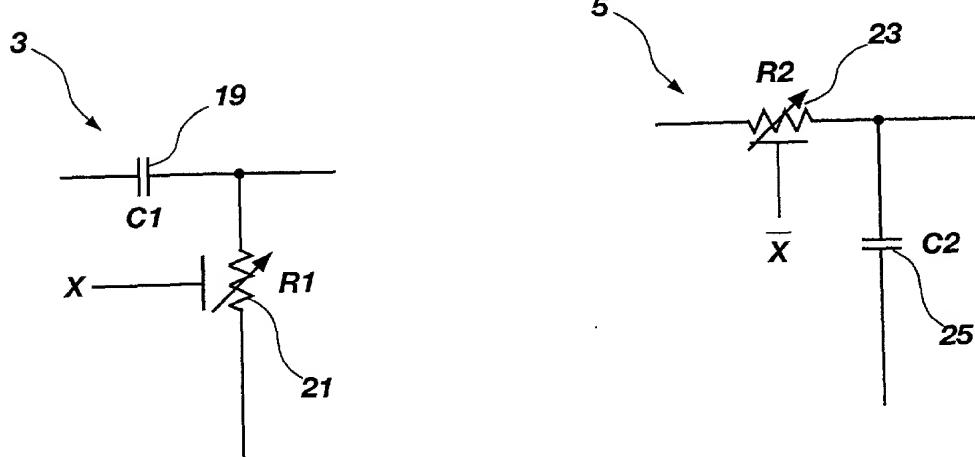


Fig. 5

Fig. 6

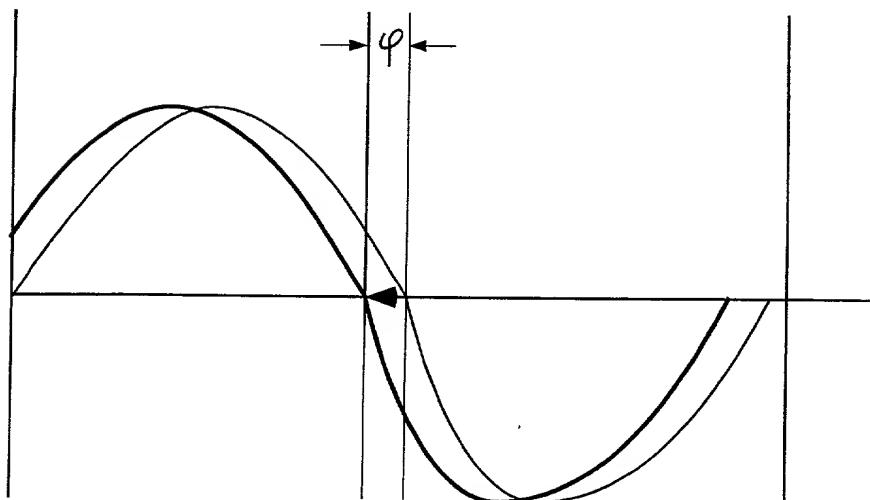


Fig. 7A

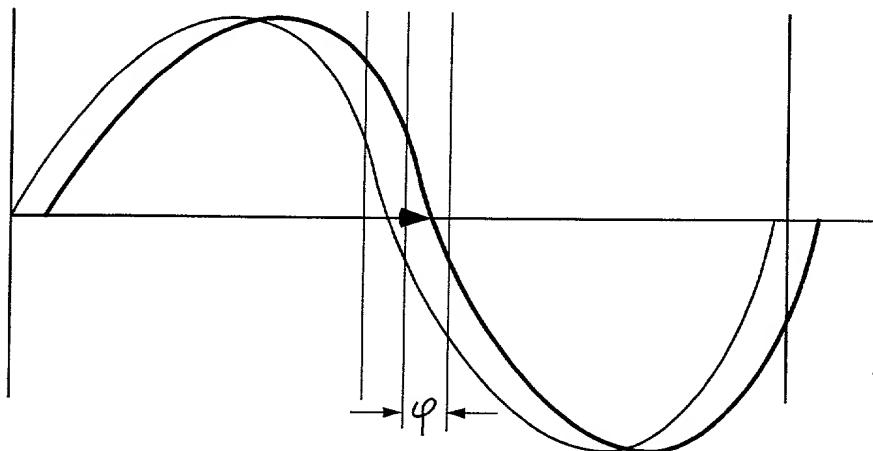


Fig. 7B

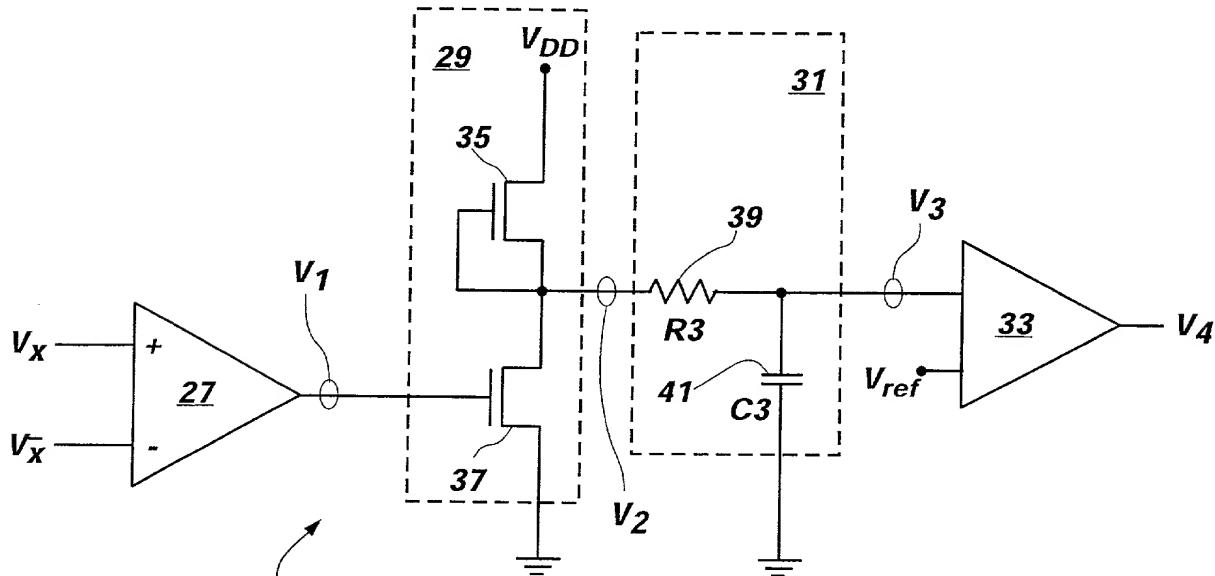


Fig. 8

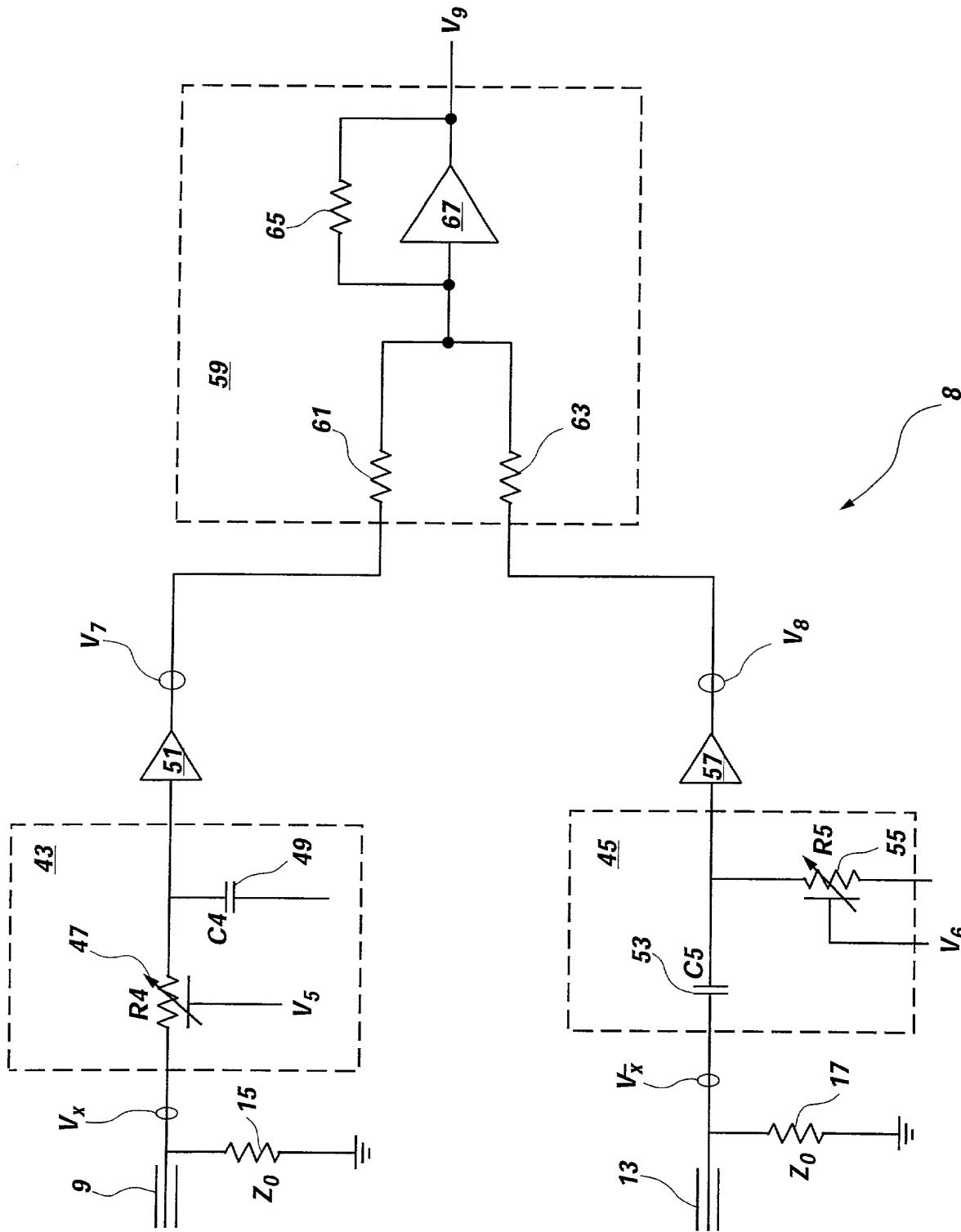


Fig. 9